L Number	Hits	Search Text	DB	Time stamp
-	159	712/22.ccls. and @ad<=20000202	USPAT;	2004/04/14
_	271	712/227.ccls. and @ad<=20000202	US-PGPUB USPAT;	08:33 2002/08/16
_	336	712/215.ccls. and @ad<=20000202	US-PGPUB USPAT;	11:43 2002/08/16
-	90	712/224.ccls. and @ad<=20000202	US-PGPUB USPAT;	11:44 2002/08/16
_	311	712/244.ccls. and @ad<=20000202	US-PGPUB USPAT;	11:44
_	619	exception and (macro same micro) and	US-PGPUB USPAT;	11:46
_	2	@ad<=20000202 exception and (macro same micro) and SSE	US-PGPUB USPAT;	11:46
	542	and @ad<=20000202 SSE and @ad<=20000202	US-PGPUB USPAT;	12:32
		-	US-PGPUB	12:32
-	3	SSE and SIMD and @ad<=20000202	USPAT; US-PGPUB	2002/08/16 12:35
-	3318	(micro same macro) and @ad<=20000202	USPAT; US-PGPUB	2002/08/16 12:35
-	41	SIMD and (micro same macro) and @ad<=20000202	USPAT; US-PGPUB	2002/08/16 13:11
-	328	SIMD and exception and parallel and @ad<=20000202	USPAT; US-PGPUB	2002/08/16
-	73	SIMD and exception and parallel and ((macro micro) near3 instruction) and	USPAT; US-PGPUB	2002/08/16
	14	@ad<=20000202	USPAT;	2002/08/16
-	14	((macro micro) near3 instruction) and	US-PGPUB	13:42
_	1	(flush cancel) and @ad<=20000202 SIMD and exception and parallel and	USPAT;	2002/08/16
		((macro micro) near3 instruction) and (flush cancel) and SSE and @ad<=20000202	US-PGPUB	13:49
-	88	exception and SSE and @ad<=20000202	USPAT; US-PGPUB	2002/08/16 13:53
-	2	exception and (SSE near10 extension) and @ad<=20000202	USPAT; US-PGPUB	2002/08/16 13:50
-	5	exception and (streaming near10 extension) and @ad<=20000202	USPAT; US-PGPUB	2002/08/16
_	173	SIMD	EPO	2002/08/16
-	26	SIMD and stream	EPO	2002/08/16 14:25
_	1998	(flush cancel) and exception and	USPAT; US-PGPUB	2002/08/16 14:35
-	114	instruction and @ad<=20000202 (flush cancel) and exception and SIMD and	USPAT;	2002/08/16
-	89	@ad<=20000202 abdallah.in.	US-PGPUB USPAT;	14:36 2002/08/16
_	11	abdallah-mohammad.in.	US-PGPUB USPAT;	14:44 2002/08/16
_	2	numerical adj exception	US-PGPUB USPAT;	14:46 2002/08/16
_	40	(macro same micro) and SIMD and parallel	US-PGPUB USPAT;	14:46 2002/08/16
_	5	and @ad<=20000202 SSE and SIMD	US-PGPUB USPAT;	15:06 2002/08/19
_	80	denormal	US-PGPUB USPAT;	11:17 2002/08/19
_	9	denormal same handler	US-PGPUB USPAT;	13:33
_	38	("82" adj1 bit) near2 register	US-PGPUB USPAT;	13:33
	3	MXCSR	US-PGPUB USPAT;	09:27
			US-PGPUB	16:06
_	1	6321327.pn.	USPAT; US-PGPUB	2002/08/20
_	1	6357016.pn.	USPAT; US-PGPUB	2002/08/20 16:24

-	1	5859999.pn.	USPAT;	2002/08/20
		5050017	US-PGPUB	16:45
-	1	5860017.pn.	USPAT;	2002/08/20
1_	80	SIMD and macro and micro and (combine	US-PGPUB USPAT;	16:45 2002/12/13
		merge) and @ad<=20000202	US-PGPUB	08:49
_	21	SIMD and (macro with micro) and (combine	USPAT;	2002/12/13
		merge) and @ad<=20000202	US-PGPUB	09:45
_	23	SIMD and (macro same micro) and (combine	USPAT;	2002/12/13
		merge) and @ad<=20000202	US-PGPUB	13:51
-	3	SIMD and (macro same micro) and (micro	USPAT;	2002/12/13
1		with (parallel simultaneous)) and	US-PGPUB	10:04
		exception and @ad<=20000202		
-	1	SIMD and ((exception same (flush cancel))	USPAT;	2002/12/13
 _	127	same micro) and @ad<=20000202 SIMD and (exception and (flush cancel))	US-PGPUB	10:06
-	127	and @ad<=20000202	USPAT; US-PGPUB	2002/12/13
_	9	SIMD and (exception same (flush cancel))	USPAT;	2002/12/13
		and @ad<=20000202	US-PGPUB	10:09
-	3	exception same (flush cancel) same soon	USPAT;	2002/12/13
		and @ad<=20000202	US-PGPUB	15:29
-	1	6338136.pn.	USPAT;	2002/12/13
	_		US-PGPUB	15:29
[-	8	((register with "82" with bit) same	USPAT;	2002/12/16
[_	0	mantissa) and @ad<=20000202	US-PGPUB	08:47
1		(exponent near5 "17" near2 bit) same (double with precision with float) and	USPAT; US-PGPUB	2002/12/16
1		(double with precision with float) and (dad<=20000202	03-26200	03.20
_	14	exception with microinstruction same	USPAT;	2003/03/25
		parallel	US-PGPUB	10:54
-	110	exception with parallel same result	USPAT;	2003/03/25
			US-PGPUB	10:55
-	5	exception with (microinstruction	USPAT;	2003/03/25
İ		micro-instruction) with parallel	US-PGPUB	11:22
-	88	(exception trap) with parallel with instruction	USPAT;	2003/03/27
1_	1	(exception trap) same parallel same	US-PGPUB USPAT;	10:04
	*	(instruction microinstruction) same	US-PGPUB	10:08
		(prevent with (updat\$3 writ\$3))	00 10105	10.00
-	142	(exception trap) same parallel same	USPAT;	2003/03/27
1		(instruction microinstruction) same	US-PGPUB	10:13
	_	(updat\$3 writ\$3 cancel)		
-	3	(exception trap) same parallel same	USPAT;	2003/03/27
		(instruction microinstruction) same cancel	US-PGPUB	10:10
_	86		USPAT;	2003/03/27
		same (updat\$3 writ\$3 cancel)	US-PGPUB	14:02
-	0	execute with all with microinstruction	USPAT;	2003/03/27
		with parallel and macroinstruction	US-PGPUB	14:03
-	0	(execute same all same microinstruction	USPAT;	2003/03/27
		same parallel) and macroinstruction	US-PGPUB	14:03
-	1	(execute same (all every each) same	USPAT;	2003/03/27
		microinstruction same parallel) and	US-PGPUB	14:04
l _	10	macroinstruction (execute same (all every each) same	USPAT;	2003/03/27
		microinstruction same parallel)	US-PGPUB	14:05
-	5	(execute with microinstruction with	USPAT;	2003/03/27
		parallel) and macroinstruction	US-PGPUB	14:13
-	0	(execut\$3 with (all each every) with	USPAT;	2003/03/27
		microinstruction with (parallel	US-PGPUB	14:15
		concurren\$3)) and macroinstruction		0000/00/00
-	33	(execut\$3 with microinstruction with	USPAT;	2003/03/27
		(parallel concurren\$3)) and macroinstruction	US-PGPUB	14:18
_	18	macroinstruction with two near3	USPAT;	2003/03/27
		microinstruction	US-PGPUB	14:21
-	3	"streaming SIMD extensions"	USPAT;	2003/03/27
		_	US-PGPUB	17:00
-	17		USPAT;	2003/12/09
	_	(conflict\$3 dependenc\$3)	US-PGPUB	16:46
-	1	5537561.pn.	USPAT;	2003/12/09
L	L		US-PGPUB	17:00

-	3	forc\$3 with issu\$3 with parallel with instruction	USPAT; US-PGPUB	2003/12/09 17:02
-	6	prevent\$3 with issu\$3 with parallel with	USPAT;	2003/12/09
-	11	instruction delay\$3 with issu\$3 with parallel with	US-PGPUB USPAT;	17:05 2003/12/09
	10	<pre>instruction delay\$3 with issu\$3 with both with</pre>	US-PGPUB USPAT;	17:07 2003/12/09
	10	instruction	US-PGPUB	17:07
-	21	requir\$3 with issu\$3 with (together simultaneous) with instruction	USPAT; US-PGPUB	2003/12/09 17:14
-	64	requir\$3 with issu\$3 with (together	USPAT;	2003/12/09
_	1	parallel simultaneous) with instruction (detect\$3 near3 exception) same result	US-PGPUB USPAT;	17:14 2003/12/10
		with temporary with (register storage	US-PGPUB	09:02
-	40	<pre>buffer) exception same result with temporary with</pre>	USPAT;	2003/12/10
	0	<pre>(register storage buffer) exception same result with ("not written"</pre>	US-PGPUB USPAT;	09:03 2003/12/10
	-	"not stored")	US-PGPUB	09:04
-	0	<pre>exception same result with ("not writ\$3" "not stor\$3")</pre>	USPAT; US-PGPUB	2003/12/10 09:04
-	О	exception same result with ("not" with	USPAT;	2003/12/10
_	1650	<pre>(writ\$3 stor\$3)) exception same result with ((writ\$3</pre>	US-PGPUB USPAT;	09:04 2003/12/10
	38	<pre>stor\$3)) exception same result with ((writ\$3</pre>	US-PGPUB USPAT;	09:05 2003/12/10
		stor\$3) with temporary)	US-PGPUB	09:06
-	5	<pre>exception same result with (without with (writ\$3 stor\$3) with temporary)</pre>	USPAT; US-PGPUB	2003/12/10 09:07
-	23	exception same result with (without near2	USPAT;	2003/12/10
-	3	<pre>(writ\$3 stor\$3)) instruction with forc\$3 with issu\$3 with</pre>	US-PGPUB USPAT;	10:01 2004/04/13
_	55	<pre>parallel instruction with issu\$3 with parallel</pre>	US-PGPUB USPAT;	15:21 2003/12/10
		with (requir\$3 prevent\$3 forc\$3)	US-PGPUB	11:05
-	110	knebel.in.	USPAT; US-PGPUB	2003/12/10 11:05
-	10	knebel-pa\$.in.	USPAT;	2003/12/10
-	5	("5613117" "5859999" "5860017"	US-PGPUB USPAT	11:05 2003/12/10
_	35	"5922065" "6237077").PN. (stall\$3 delay\$3 prevent\$3) with	USPAT;	11:15 2004/04/13
	55	(dispatch\$3 issu\$3) with (instruction	US-PGPUB	15:28
		microinstruction) with (together simultaneouely parallel) and g06f\$.ipc.		
-	17	(dispatch\$3 issu\$3) with (instruction	USPAT;	2004/04/13
		microinstruction) with ((both all) and none) and g06f\$.ipc.	US-PGPUB	15:36
-	0	((issu\$3 dispatch\$3) near5 only with pair with instruction) same (stall\$3 prevent\$3	USPAT; US-PGPUB	2004/04/13 15:39
		delay\$3) and g06f\$.ipc.		
-	38	((issu\$3 dispatch\$3) with pair with instruction) same (stall\$3 prevent\$3	USPAT; US-PGPUB	2004/04/13 15:50
	20	delay\$3) and g06f\$.ipc.		
-	20	((issu\$3 dispatch\$3) near3 instruction near3 together) same (stall\$3 prevent\$3	USPAT; US-PGPUB	2004/04/13 15:52
_	54	<pre>delay\$3) and g06f\$.ipc. ((issu\$3 dispatch\$3) near3 instruction</pre>	USPAT;	2004/04/13
	77	near3 (stall\$3 prevent\$3 delay\$3 wait\$3)	US-PGPUB	15:57
-	0	with ready) and g06f\$.ipc. ((forc\$3 must requir\$3) with (issu\$3	USPAT;	2004/04/13
	-	dispatch\$3) near3 instruction with (pair	US-PGPUB	15:58
		together)) same (stall\$3 prevent\$3 delay\$3 wait\$3) and g06f\$.ipc.		
-	14	((forc\$3 must requir\$3) with (issu\$3 dispatch\$3) near3 instruction with (pair	USPAT; US-PGPUB	2004/04/13 16:05
	امتما	together)) and g06f\$.ipc.		
-	316	((forc\$3 must requir\$3) with (issu\$3 dispatch\$3) with (simultaneous\$2 pair	USPAT; US-PGPUB	2004/04/13 16:06
		together)) and g06f\$.ipc.		

-	144	1 , , , , , , , , , , , , , , , , , , ,	USPAT;	2004/04/13
		dispatch\$3) with (pair together)) and	US-PGPUB	16:06
	_	g06f\$.ipc.		
-	5	(((USPAT;	2004/04/13
		dispatch\$3) with microinstruction with	US-PGPUB	16:14
		(pair together)) and g06f\$.ipc.		
-	5	(USPAT	2004/04/13
		"5922065" "6237077").PN.		16:08
-	0	6618801.URPN.	USPAT	2004/04/13
	1			16:10
-	1	5860017.pn.	USPAT;	2004/04/13
			US-PGPUB	16:15
-	0	(pair together) with wait\$3 with ready	USPAT;	2004/04/13
		with (issu\$3 dispatch\$3) with	US-PGPUB	16:26
		(instruction microinstruction) and		1
		g06f\$.ipc.		
-	498	ready with (issu\$3 dispatch\$3) with	USPAT;	2004/04/13
		(instruction microinstruction) and	US-PGPUB	16:26
		g06f\$.ipc.		
_	1450	(issu\$3 dispatch\$3) with ((first and	USPAT;	2004/04/13
		second) with (instruction	US-PGPUB	16:27
		microinstruction)) and g06f\$.ipc.		
_	331	(issu\$3 dispatch\$3) with ((first and	USPAT;	2004/04/13
		second) adj1 (instruction	US-PGPUB	16:28
		microinstruction)) and g06f\$.ipc.		
_	27		USPAT;	2004/04/13
		with (issu\$3 dispatch\$3) with ((first and	US-PGPUB	16:36
		second) adj1 (instruction		
		microinstruction)) and g06f\$.ipc.		
_	58	(stall\$3 delay\$3 wait\$3 prevent\$3) with	USPAT;	2004/04/13
1		(issu\$3 dispatch\$3) with (bundle group)	US-PGPUB	16:37
		with (instruction microinstruction) and		
		q06f\$.ipc.		
_	216		USPAT;	2004/04/14
			US-PGPUB	08:33
	1	<u></u>	00 10100	00.00